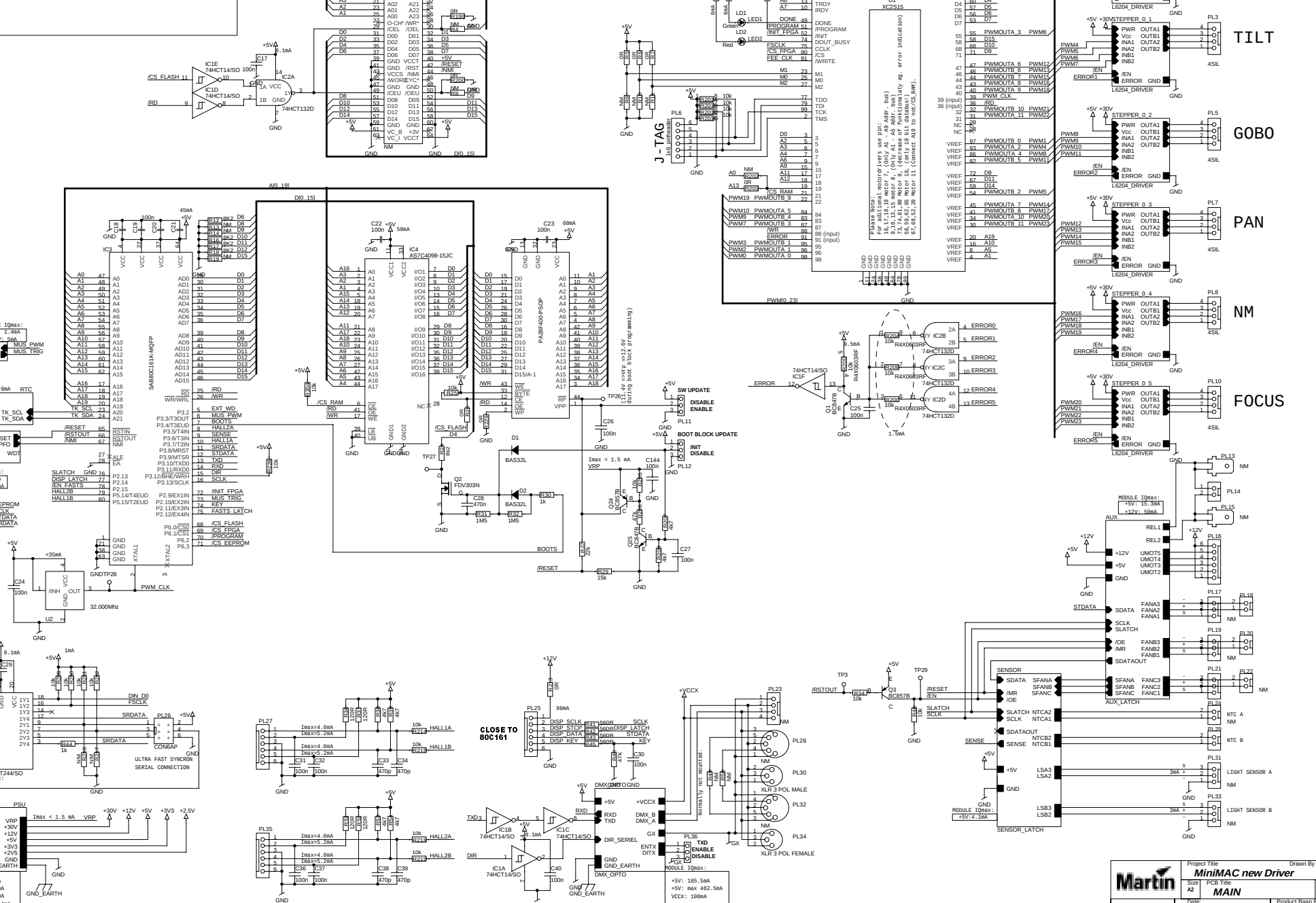


7

1

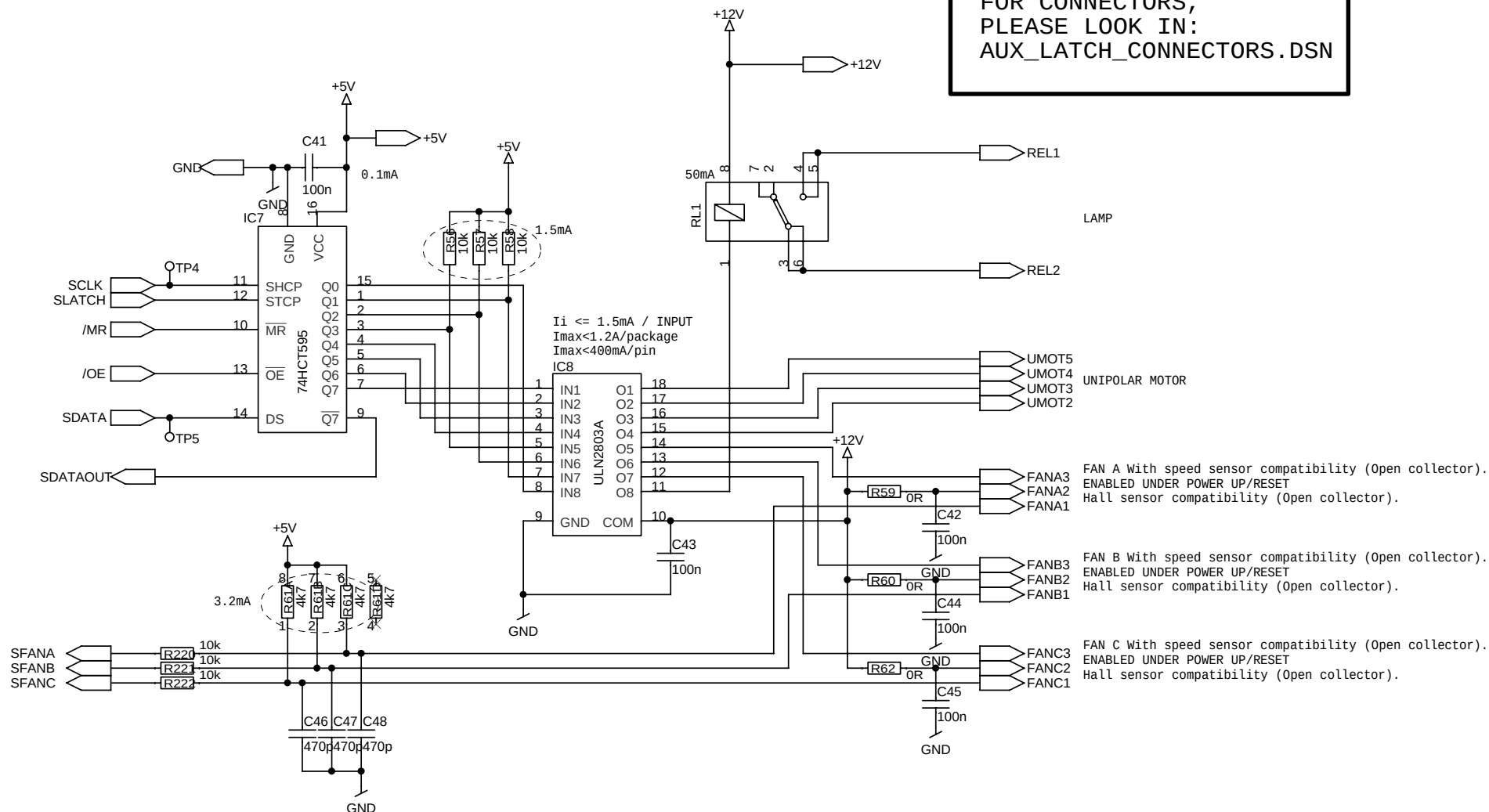
ROT.GOB0



+5V: 15.3mA
+12V: 50mA

ADD:
3x10k resistors

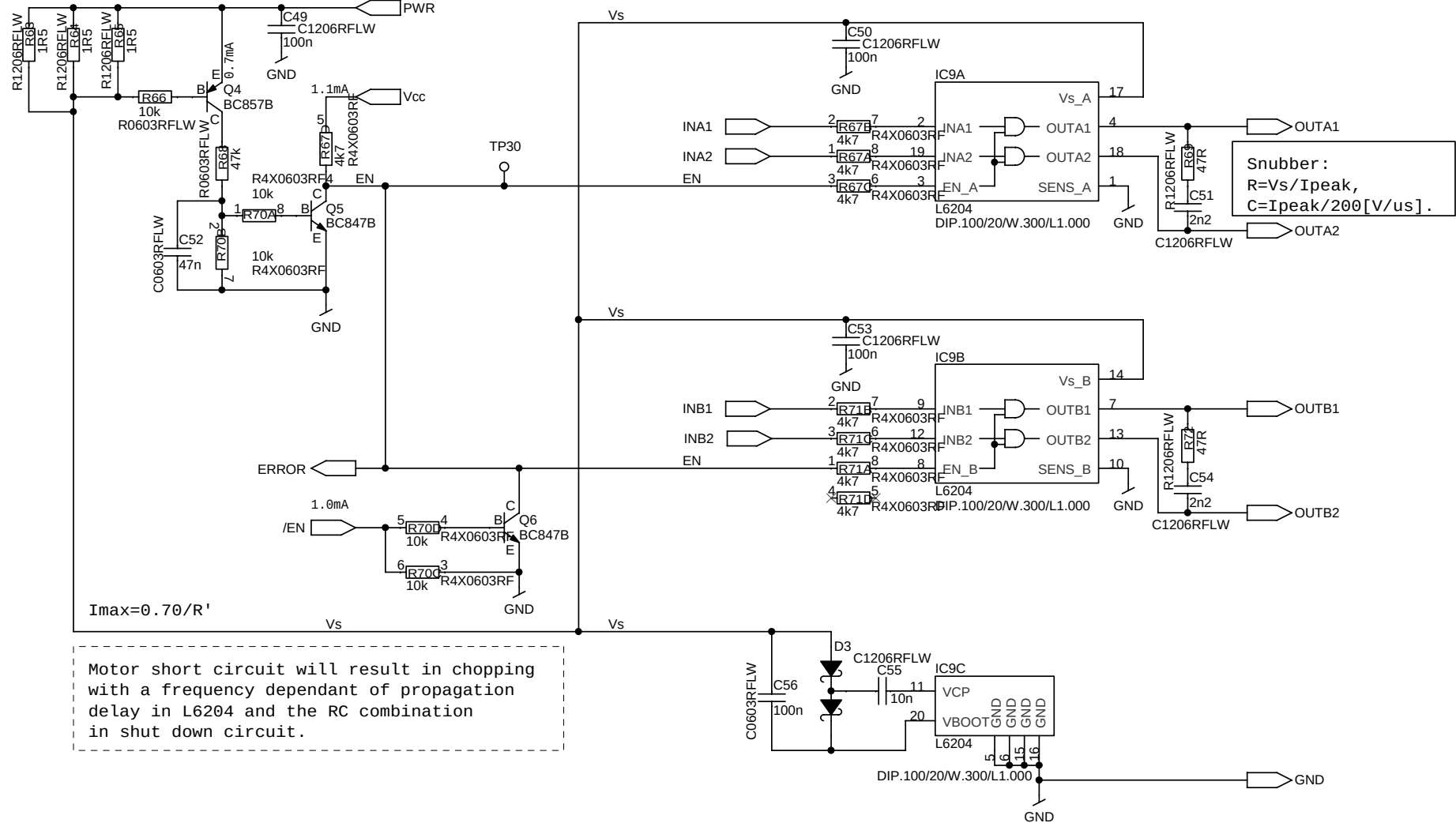
FOR CONNECTORS,
PLEASE LOOK IN:
AUX_LATCH_CONNECTORS.DSN



MODULE IQmax:

+VCC: 2.1mA.
+PWR: 10.7mA.
Iin(+3.3V): 1.5mA.

OVERLOAD DETECTION WITH DELAYED SHUTDOWN

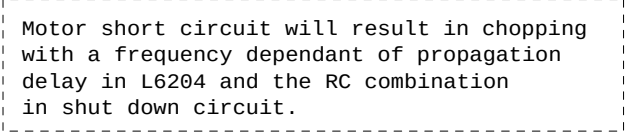


Changes from Prototype:

Capacitor over enable transistor is changed from 100n to 47n (minimun X7R)
Sense resistors is changed from two 1.0 ohm to tree 1.5 ohms.

		Project Title		Drawn By	
		L6204_DRIVER		TV	
Sheet 3 of 15		Size A4	PCB Title		Rev D
		Design template			
Date: Monday, February 04, 2002		Product Baan Number			
		None			

+VCC: 2.1mA.
+PWR: 10.7mA.
Iin(+3.3v): 1.5mA.

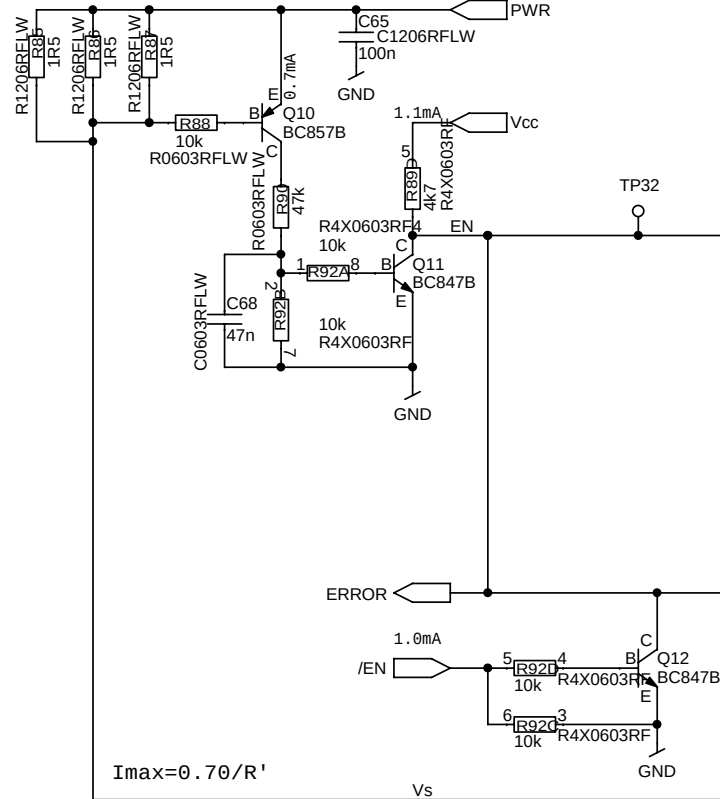


Capacitor over enable transistor is changed from 100n to 47n (minimum X7R)
Sense resistors is changed from two 1.0 ohm to three 1.5 ohms.

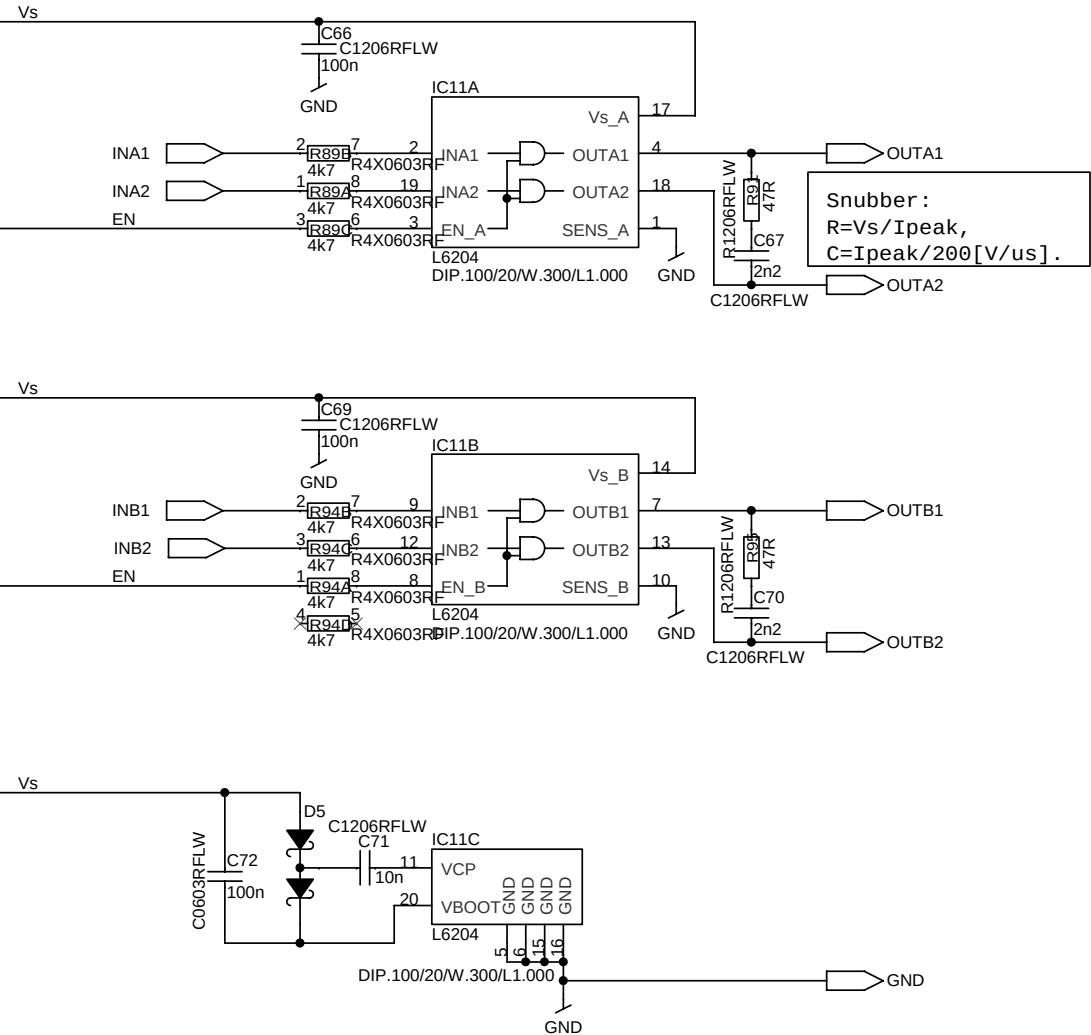
MODULE IQmax:

+VCC: 2.1mA.
+PWR: 10.7mA.
Iin(+3.3V): 1.5mA.

OVERLOAD DETECTION WITH DELAYED SHUTDOWN



Motor short circuit will result in chopping with a frequency dependant of propagation delay in L6204 and the RC combination in shut down circuit.



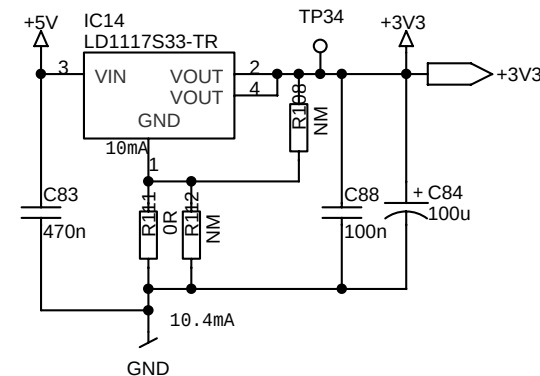
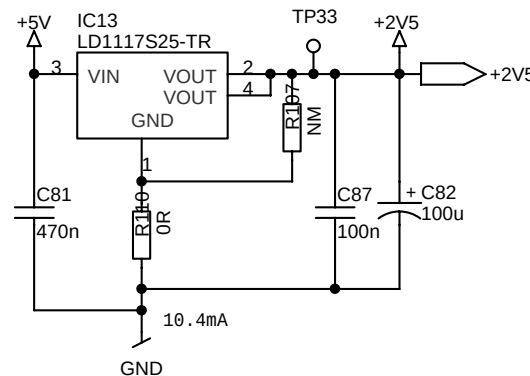
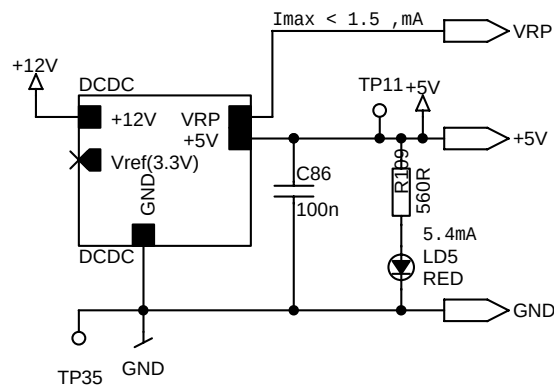
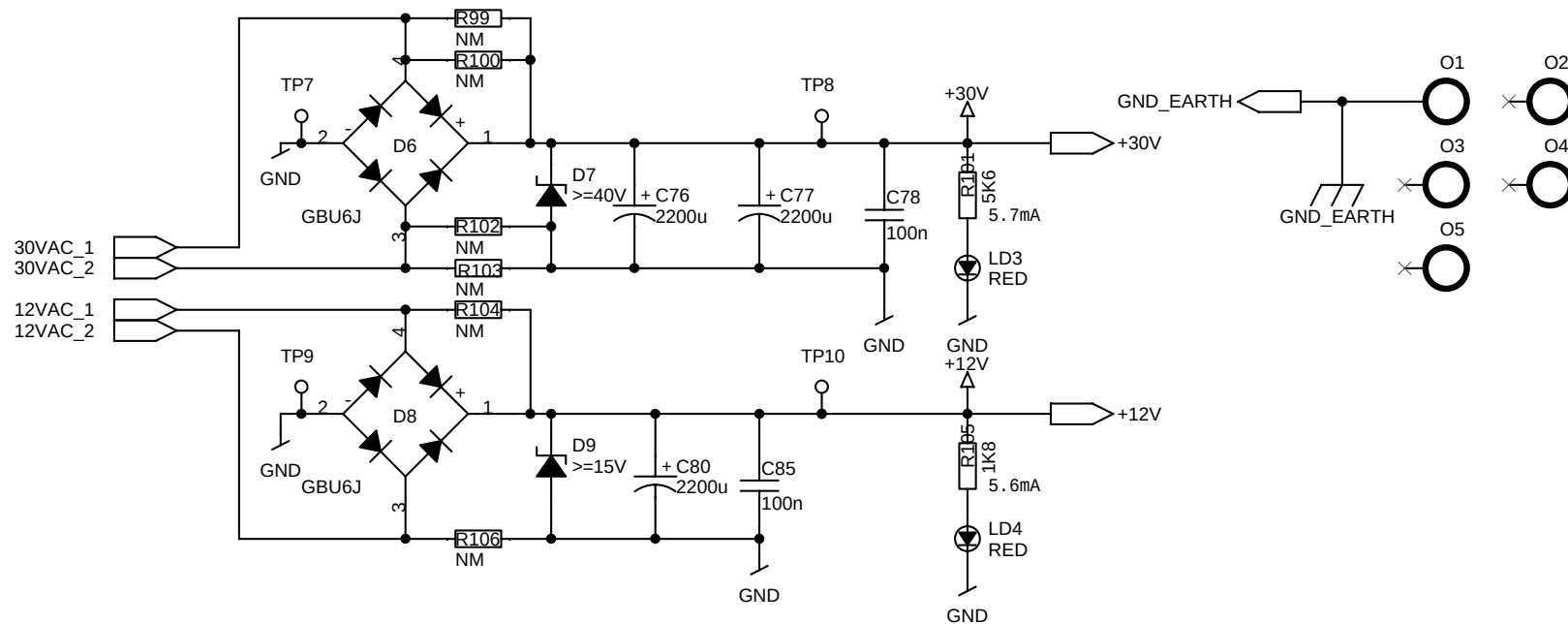
Changes from Prototype:

Capacitor over enable transistor is changed from 100n to 47n (minimun X7R)
Sense resistors is changed from two 1.0 ohm to tree 1.5 ohms.

	Project Title		Drawn By	
	L6204_DRIVER		TV	
Sheet 5 of 15	Size A4	PCB Title		Rev D
	Design template			
	Date: Monday, February 04, 2002		Product Baan Number None	

MODULE IQmax:

+5V: 5.7mA
 +12V: 5.6mA
 +30V: 5.7mA
 +2.5V: 10.4mA
 +3.3V: 10.4mA



Changes from prototype:

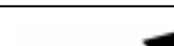
Not mounted:
 R107, R108, R112, R99, R100, R102, R103, R104, R106

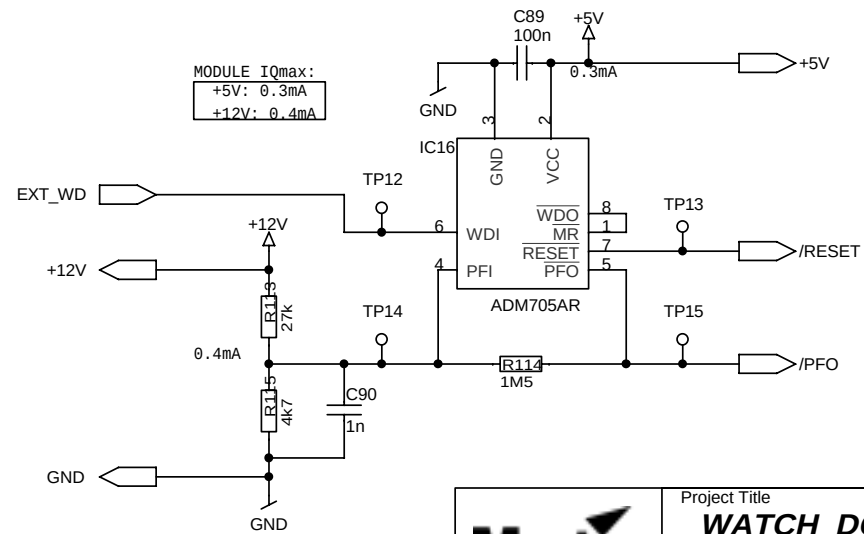
Footprint changes:
 R105, R107, R108, R109, R110, R111, C81, C83

Value:
 R81, C83

NET:
 VRP for flash boot blok write enable.

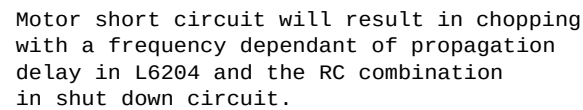
Deleted:
 IC15.

	Project Title		Drawn By	
	MINIMAC PSU		TV	
	Size A	PCB Title		Rev D
		PSU		
Sheet 7 of 15	Date:		Product Baan Number	
	Monday, February 04, 2002		01001521	

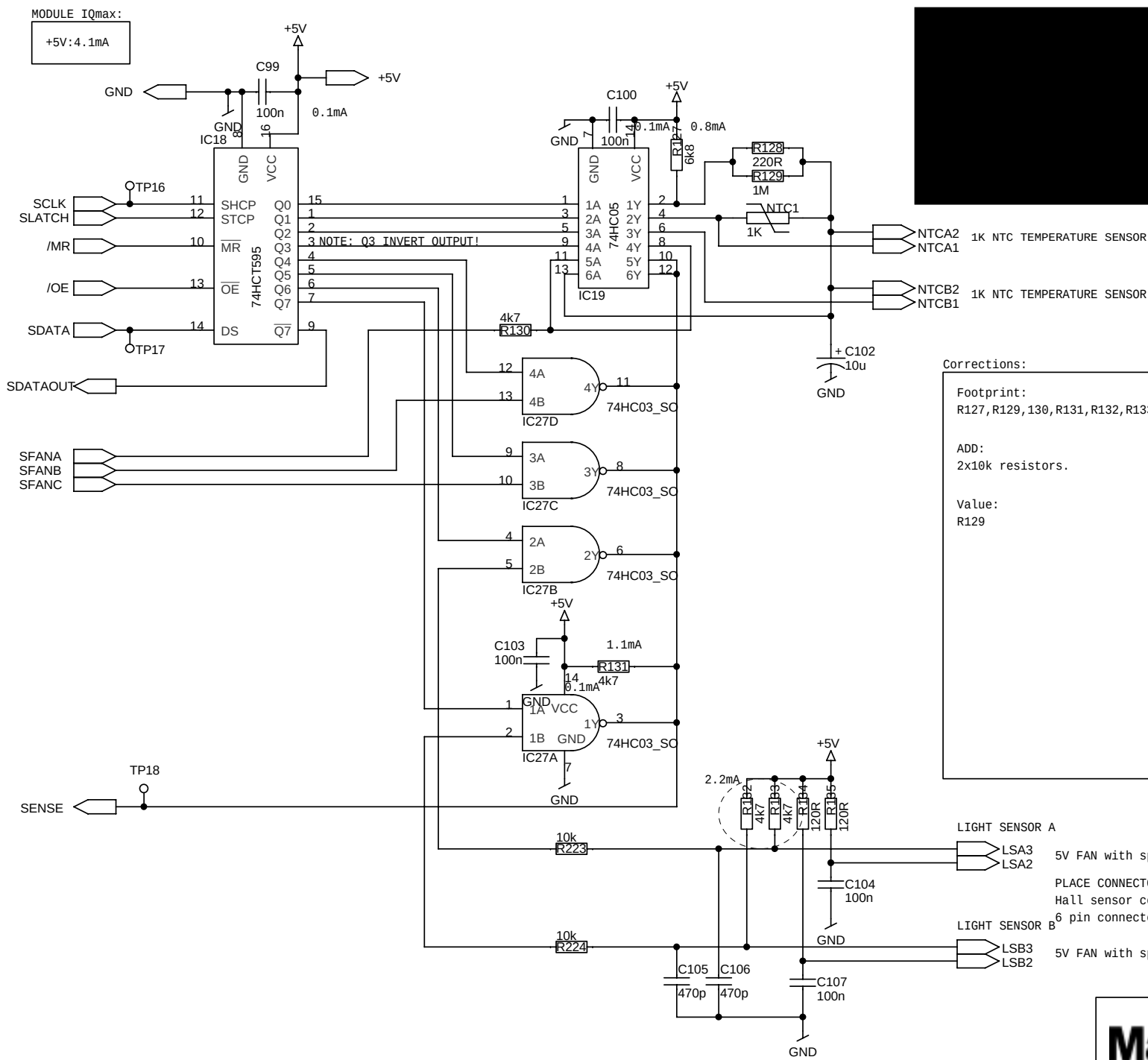


	Project Title		Drawn By
	WATCH_DOG		TV
	Size A4	PCB Title Design template	Rev D
Date: Monday, February 04, 2002		Product Baan Number None	
Sheet 8 of 15			

+VCC: 2.1mA.
+PWR: 10.7mA.
Iin(+3.3v): 1.5mA.



Capacitor over enable transistor is changed from 100n to 47n (minimum X7R)
Sense resistors is changed from two 1.0 ohm to three 1.5 ohms.



Corrections:

Footprint:
R127, R129, 130, R131, R132, R133,

ADD:
2x10k resistors.

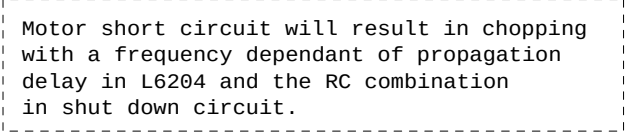
Value:
R129

LIGHT SENSOR A
LSA3
LSA2
5V FAN with speed sensor compatibility (TURN 3 PIN CONNECTOR 180 DEGREES!).
PLACE CONNECTORS CLOSE TOGETHER.
Hall sensor compatibility! (turn 6 pin connector 180 degrees).

LIGHT SENSOR B
LSB3
LSB2
5V FAN with speed sensor compatibility (TURN 3 PIN CONNECTOR 180 DEGREES!).

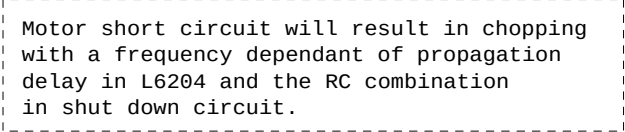
	Project Title		Drawn By	
	SENSOR MODULE		TV	
	Size A4	PCB Title		Rev D
Design template		Date:	Product Baan Number	
Monday, February 04, 2002		None		
Sheet 10 of 15				

+VCC: 2.1mA.
+PWR: 10.7mA.
Iin(+3.3v): 1.5mA.



Capacitor over enable transistor is changed from 100n to 47n (minimun X7R)
Sense resistors is changed from two 1.0 ohm to tree 1.5 ohms.

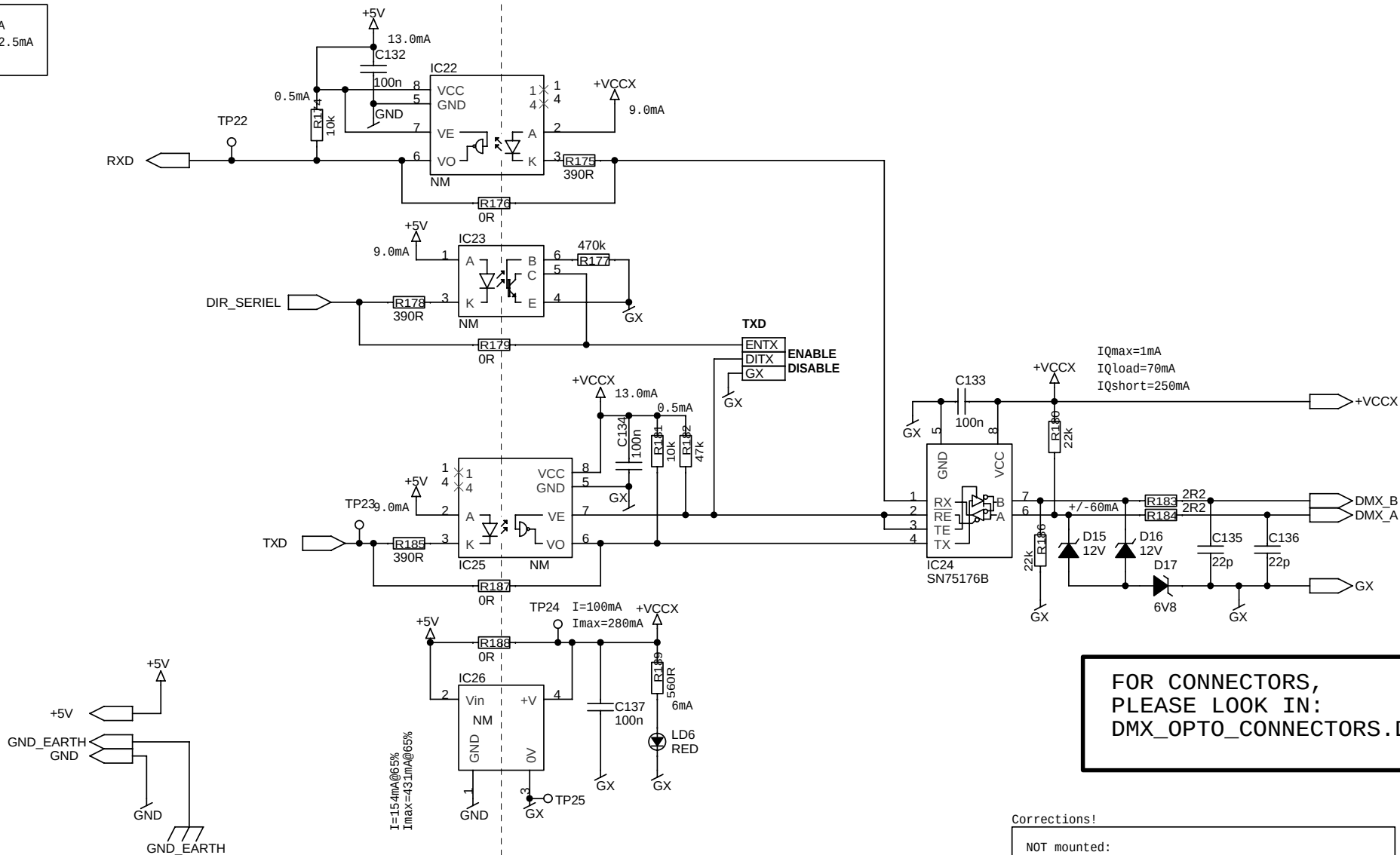
+VCC: 2.1mA.
+PWR: 10.7mA.
Iin(+3.3v): 1.5mA.



Capacitor over enable transistor is changed from 100n to 47n (minimum X7R)
Sense resistors is changed from two 1.0 ohm to three 1.5 ohms.

MODULE IQmax:

+5V: 185.5mA
+5V: max 462.5mA
VCCX: 100mA



FOR CONNECTORS,
PLEASE LOOK IN:
DMX_OPTO_CONNECTORS.DSN

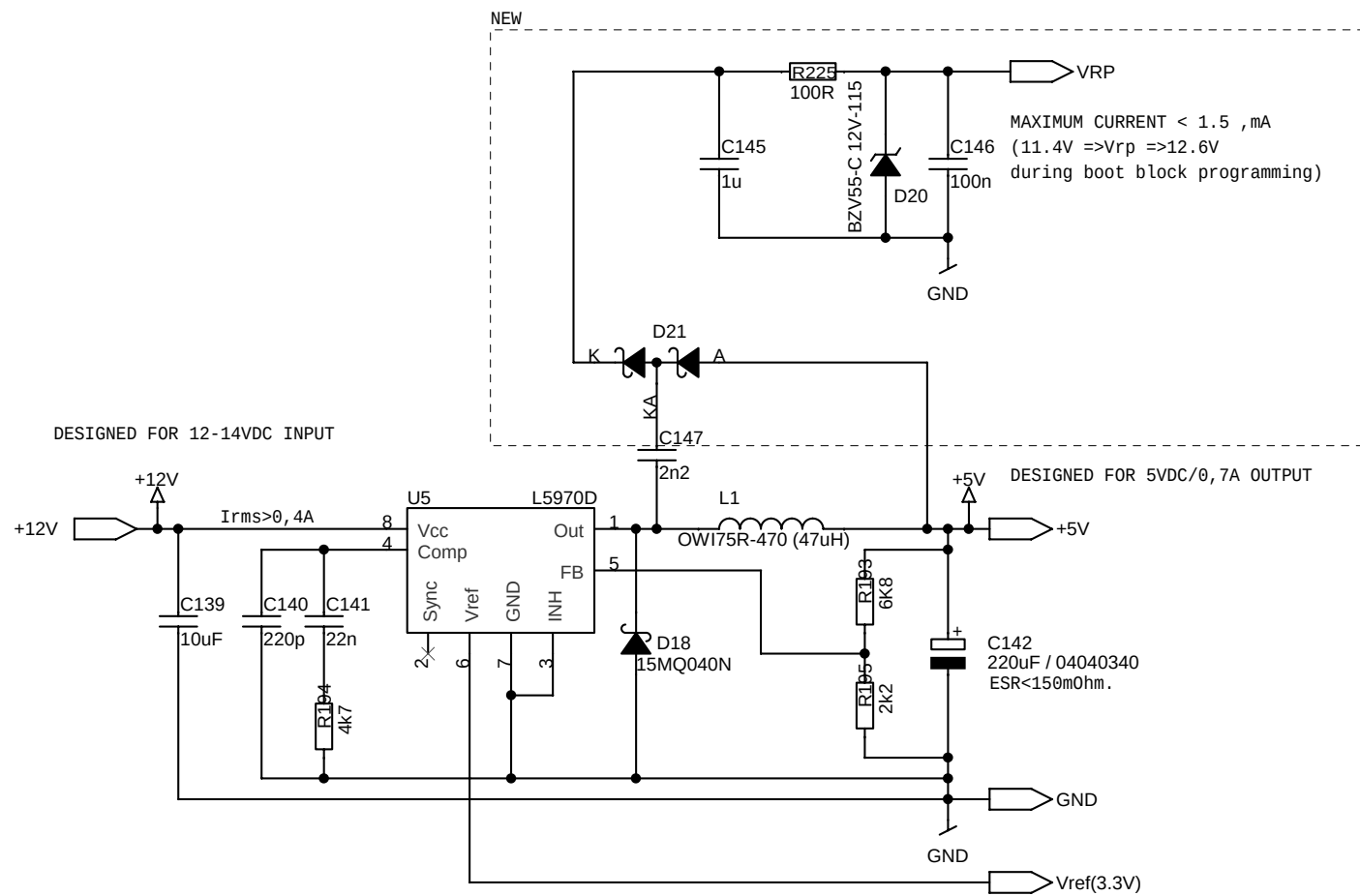
Corrections!

NOT mounted:
C132, R175, IC22, R177, IC23, R178, C134,
IC25, R185, IC188, C137, R191
Footprint changes:
R175, R178, R185, R189, C138, C135, C136.
Value:
C135, C136.

FOR ISOLATED TRANSCEIVER:
Capacitor and only this connection
between GND and GND_earth is ALLOWED.

FOR GROUNDED TRANSCEIVER:
0 Ohm resistors are mounted.
Capacitor and 100 Ohm connection between
GX and GND_EARTH are mounted.
NOTE! Connection between GND and
GND_EARTH IS NOT ALLOWED.

	Project Title		Drawn By	
	DMX_OPTO		TV	
Size A4	PCB Title		Rev	
	Design template		D	
Date:		Product Baan Number		
Wednesday, February 06, 2002		none		



Changes from prototype:

Circuit:
VRP for flash boot blok write enable.

	Project Title		Drawn By
	12V to 5V DC/DC		TV
Size A	PCB Title		Rev D
	MiniMAC New driver		
Date:		Product Baan Number	
Monday, February 04, 2002		none	